

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/617,328	07/10/2003	Yasuhiro Miki	9281/4608	2828
75	590 10/13/2004		EXAMINER	
Brinks Hofer Gilson & Lione			CALEY, MICHAEL H	
P.O. Box 10395 Chicago, IL 6			ART UNIT	PAPER NUMBER
J			2871	-
			DATE MAILED: 10/13/2004	4

Please find below and/or attached an Office communication concerning this application or proceeding.

			702		
	Application No.	Applicant(s)			
	10/617,328	MIKI ET AL.			
Office Action Summary	Examiner	Art Unit			
	Michael H. Caley	2871			
The MAILING DATE of this communication a Period for Reply	appears on the cover sheet wi	th the correspondence address			
A SHORTENED STATUTORY PERIOD FOR REF THE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a - If NO period for reply is specified above, the maximum statutory peri - Failure to reply within the set or extended period for reply will, by sta Any reply received by the Office later than three months after the may earned patent term adjustment. See 37 CFR 1.704(b).	N. 1.136(a). In no event, however, may a r reply within the statutory minimum of thirt iod will apply and will expire SIX (6) MON tute, cause the application to become AB	eply be timely filed y (30) days will be considered timely. THS from the mailing date of this communication ANDONED (35 U.S.C. § 133).	ation.		
Status					
1) Responsive to communication(s) filed on					
	his action is non-final.				
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits					
closed in accordance with the practice unde	er <i>Ex parte Quayle</i> , 1935 C.D	. 11, 453 O.G. 213.			
Disposition of Claims					
4)⊠ Claim(s) <u>1-3</u> is/are pending in the applicatio	n.				
4a) Of the above claim(s) is/are withd	rawn from consideration.				
5) Claim(s) is/are allowed.					
6)⊠ Claim(s) <u>1-3</u> is/are rejected.					
7) Claim(s) is/are objected to.					
8) Claim(s) are subject to restriction and	d/or election requirement.				
Application Papers					
9)☐ The specification is objected to by the Exam	iner.				
10)⊠ The drawing(s) filed on 10 July 2003 is/are:	a)⊠ accepted or b)☐ objec	ted to by the Examiner.			
Applicant may not request that any objection to t	he drawing(s) be held in abeyar	ice. See 37 CFR 1.85(a).			
Replacement drawing sheet(s) including the corr	ection is required if the drawing	(s) is objected to. See 37 CFR 1.12	21(d).		
11)☐ The oath or declaration is objected to by the	Examiner. Note the attached	I Office Action or form PTO-152	<u>)</u> .		
Priority under 35 U.S.C. § 119					
 12) Acknowledgment is made of a claim for foreit a) All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 	ents have been received.				
3. Copies of the certified copies of the p		··· 			
application from the International Bure	•	received in this National Stage			
* See the attached detailed Office action for a I	. , , , ,	received.			
Attachment(s)					
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) 		Summary (PTO-413) S)/Mail Date			
 Notice of Draitsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB//Paper No(s)/Mail Date <u>07102003</u>. 		offormal Patent Application (PTO-152)			

Art Unit: 2871

DETAILED ACTION

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-3 are rejected under 35 U.S.C. 103(a) as being unpatentable over Nakahara et al. (U.S. Patent No. 5,982,470 "Nakahara") in view of Sasuga et al. (U.S. Patent No. 5,680,183 "Sasuga").

Regarding claim 1, Nakahara discloses a liquid crystal display device having:

a pair of substrates (Figure 27 elements 1 and 2) opposing each other with a gap therebetween, a liquid crystal layer being held between the pair of substrates (Figure 10 element 9);

transparent electrodes (Figures 10 and 11 elements 3 and 4) provided on the liquid crystal layer side of each of the pair of substrates so that the transparent electrodes on one of the substrates intersect the transparent electrodes on the other substrate;

metal lead wirings (Figure 26 element 19) provided on one of the substrates to be connected to the transparent electrodes on the one substrate so that ends of the transparent electrodes on the one substrate are connected to the lead wirings to form connected portions (Figure 26 elements 19 and 4);

a transparent dummy electrode provided for controlling the gap at a position on the other substrate opposite to a connection portion between the transparent electrodes Art Unit: 2871

and the lead wirings on the one substrate (Figures 26 and 27 element 44, beneath element 20);

wherein the transparent dummy electrode is formed to avoid positions opposite to the connection portions (Figures 26 and 27).

Nakahara fails to disclose the transparent electrodes connected to the metal lead wirings as overlapped on the lead wirings to form overlap portions at the connection portions. Sasuga, however, teaches such overlap portions as a conventional method of connecting a transparent electrode to a metal lead as a means of minimizing the probability of breakage of the conductive line (Column 12 lines 24-34; Figures 10 and 19 elements d1, d2, and d3).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to have formed the connecting portions disclosed by Nakahara as overlapping portions as taught by Sasuga. Sasuga teaches overlapping metal leads to the transparent electrode as a means of assuring an efficient contact between the leads and the electrode with minimal likelihood of breakage of the connection, consistent with conventional wiring connection methods of the prior art. One would have been motivated to overlap the metal lead and the transparent electrode to minimize the probability of failure and extend the life of the device while benefiting from the expected results of such a construction.

Regarding claim 2, Nakahara discloses the transparent dummy electrode (Figures 26 and 27 element 44, beneath element 20) is also provided on portions opposite to spaces between the ends of the transparent electrodes on the one substrate.

Regarding claim 3, Nakahara discloses the transparent electrodes on the one substrate as

wider than the lead wirings (Figure 26 elements 19 and 4; Column 8 lines 5-24).

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's

disclosure.

U.S. Patent No. 5,619,358 to Tanaka et al. as an alternative embodiment of a dummy

electrode (Figure 10 element 38a; Figure 2 element 26) used for controlling the gap between

substrates, avoiding the position opposite to the conventional overlap portion (Figure 11 element

34; Figure 2 element 24).

U.S. Patent Application No. 2002/0080319 to Hagiwara as an alternative embodiment of

a dummy electrode used for controlling the gap between substrates.

U.S. Patent No. 5,396,356 to Fukuchi as an alternative embodiment of a dummy

electrode used for controlling the gap between substrates.

Contact Information

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Michael H. Caley whose telephone number is (571) 272-2286.

The examiner can normally be reached on M-F 8:30 a.m. - 5:00 p.m..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Kim can be reached on (571) 272-2293. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

muc

Michael H. Caley October 2, 2004

TARIFUR R. CHOWDHURY